Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2	(HDLC or "high level data link control") and "shift register" and (SOF or "start of frame") and (EOF or "end of frame") and (clock or "clock cycle") and ("packer logic" or "packer logic unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
L3	3	"shift register" and (payload or "payload data bit") and (clock or "clock cycle") and ("packer logic" or "packer logic unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
L4	1	"shift register" and (detect NEAR7 (payload or "payload data bit")) and (clock or "clock cycle") and ("packer logic" or "packer logic unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
L5	0	"shift register" and (detect ADJ (payload or "payload data bit")) and (clock or "clock cycle") and ("packer logic" or "packer logic unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
L6	32939	((370/474) or (370/464) or (370/395.5) or (370/470) or (370/471) or (370/473) or (370/476) or (370/352) or (370/469) or (370/236) or (370/522) or (370/437) or (370/442) or (370/412) or (370/413) or (370/415) or (370/417) or (370/252) or (370/229) or (370/230) or (370/231-236) or (370/524) or (370/441) or (370/389) or (370/392) or (370/395) or (370/465) or (370/472) or (370/479) or (370/535) or (370/294) or (370/360) or (370/375) or (370/376) or (370/378) or (370/433) or (370/437) or (370/458) or (370/459) or (370/503) or (370/508) or (370/509) or (712/300) or (712/225)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:56
L7	1	L6 and (HDLC or "high level data link control") and "shift register" and (SOF or "start of frame") and (EOF or "end of frame") and (clock or "clock cycle") and ("packer logic" or "packer logic unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:57

L8	1	L6 and (HDLC or "high level data link control") and "shift register" and (SOF or "start of frame") and (EOF or "end of frame") and (clock or "clock cycle") and ("packer logic" or "packer logic unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
L9	1	L6 and "shift register" and (payload or "payload data bit") and (clock or "clock cycle") and ("packer logic" or "packer logic unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
L10	0	L6 and "shift register" and (detect ADJ (payload or "payload data bit")) and (clock or "clock cycle") and ("packer logic" or "packer logic unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
L11	1	L6 and "shift register" and (detect NEAR7 (payload or "payload data bit")) and (clock or "clock cycle") and ("packer logic" or "packer logic unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
L12	0	L6 and (HDLC or "high level data link control") and "shift register" and (SOF or "start of frame") and (EOF or "end of frame") and (clock or "clock cycle") and (@prad<"20011019" or @rlad<"20011019")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
L13	1	L6 and (HDLC or "high level data link control") and "shift register" and (SOF or "start of frame") and (EOF or "end of frame") and (clock or "clock cycle")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:59
L14	132	L6 and (HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:59
L15	0	L6 and (HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle") and (deframer or "deframer unit") and (comparators or comparator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:59

L16	0	L6 and (HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle") and (deframer or "deframer unit") and (comparators or comparator) and (@prad<"20011019" or @rlad<"20011019")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:59
L17	4	L6 and (HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle") and (deframer or "deframer unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 11:00
L18	1	L6 and (HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle") and (deframer or "deframer unit") and (@prad<"20011019" or @rlad<"20011019")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 11:00
S12 8	2	(HDLC or "high level data link control") and "shift register" and (SOF or "start of frame") and (EOF or "end of frame") and (clock or "clock cycle")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:59
S12 9	0	(HDLC or "high level data link control") and "shift register" and (SOF or "start of frame") and (EOF or "end of frame") and (clock or "clock cycle") and (@prad<"20011019" or @rlad<"20011019")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:58
S13 0	450	(HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:59
S13 1	5	(HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle") and (deframer or "deframer unit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 11:00
S13 2	0	(HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle") and (deframer or "deframer unit") and (comparators or comparator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/04 10:59

| S133 | 2 | (HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle") and (deframer or "deframer unit") and (@prad<"20011019" or @rlad<"20011019") | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/08/04 11:00 |
| S134 | 0 | (HDLC or "high level data link control" or "bit synchronous HDLC engine" or "HDLC engine") and "shift register" and (clock or "clock cycle") and (deframer or "deframer unit") and (comparators or comparator) and (@prad<"20011019" or @rlad<"20011019") | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/08/04 10:59 |